

Advance Information

Demodulator

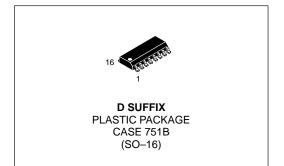
The MC44306 is an IF amplifier and mixer circuit intended for demodulation of QAM, VSB or GPSK digitally modulated signals. Great care was applied to this design to provide the best possible linearity, bandwidth.

- 60 dB Voltage Gain IF Amplifier
- 10 MHz Detectors for QAM, VSB or Analog Signals
- Complementary Buffered Mixer Outputs
- Continuous AGC with Adjustable Delay for RF Stage
- Oscillator at "Half IF" to Minimize Spurious Feedback
- VCO Frequency Range 35 to 55 MHz

MC44306

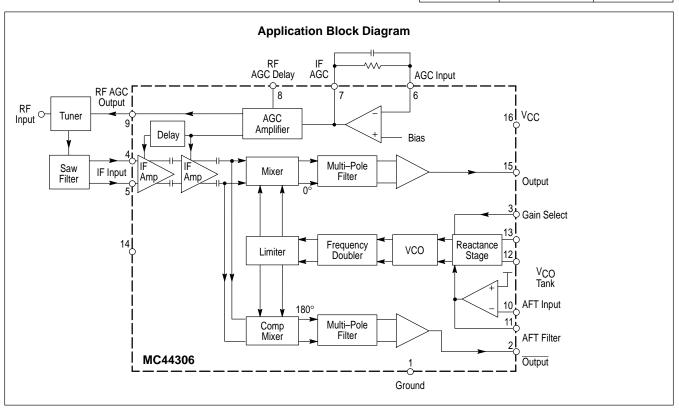
IF AMPLIFIER AND DEMODULATOR

SEMICONDUCTOR TECHNICAL DATA



ORDERING INFORMATION

| Device | Temperature Range | Package |
|----------|----------------------|---------|
| MC44306D | 0° to +70°C | SO-16 |



MC44306

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|-------------------------------------|-------------------------|------------|
| Power Supply Voltage | VCC | 7.0 | V |
| Input Voltage Range IF Input, AGC Input, AFT Input, Gain Select, RF AGC Delay, Quadrature Adjust | V _{in} | −0.5 to V _{CC} | V |
| VCO Coil Voltage | VCO | Vcc | V |
| Output Current Outputs RF AGC, Internally Limited | - | 15 2.0 | mA |
| Power Dissipation at T _A = 70°C | P _D R _θ JA | 800 100 | mW °C/W |
| Operating Junction Temperature | TJ | +150 | °C |
| Operating Ambient Temperature | TA | 0 to +70 | °C |

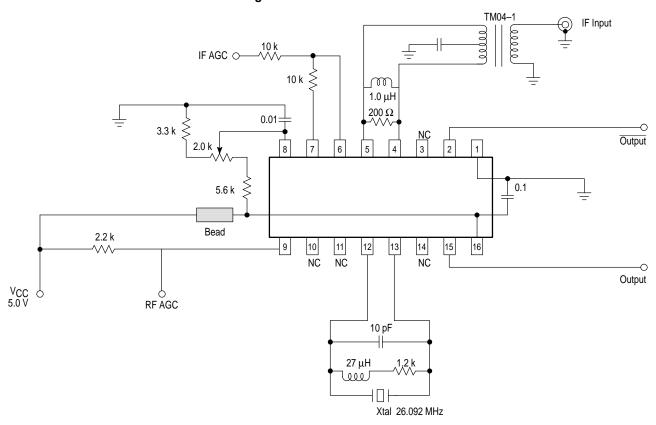
NOTE: ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0 \text{ V}$, $T_A = 25^{\circ}\text{C}$, $f_{|F} = 44 \text{ MHz}$, $f_{VCO} = 22 \text{ MHz}$, unless otherwise noted.)

| Characteristic | Symbol | Тур | Unit |
|--|------------------------------------|------------|------------|
| IF AMPLIFIER | | | <u> </u> |
| Differential Input Impedance | R _{in} C _{in} | 2.0 3.0 | kΩ pF |
| Differential Input for Full Output – 1.0 Vpp | V _{in} | 0.6 | mVrms |
| Automatic Gain Control Range | AGC | 60 | dB |
| Noise Figure (IF Input, Sourced by 900 Ω in Parallel with 5.0 pF) | NF | 5.0 | dB |
| Bandwidth (Lower and Upper Limits) IF Amplifier | BW | 35 to 120 | MHz |
| DETECTORS | | | <u>'</u> |
| Output Voltage R _L ≥ 1.0 kΩ | _ | 2.0 | Vpp |
| Distortion (CW Input 5.0 mVrms, VCO Unlocked, Adjust AGC for 2.0 Vpp Output Beat Note), all Harmonies | THD | 2.0 -34 | % dB |
| Flatness (f _{MOD} = 0 to 2.5 MHz) | - | 0.5 | dB |
| Relative Group Delay | _ | 5.0 | ns |
| Output 3.0 dB Bandwidth | BW | 10 | MHz |
| Spurious and IF Harmonics (Ref. to 2.0 Vpp Output) | - | -40 | dB |
| AGC | | | • |
| RF AGC Output (Sink) | - | 2.0 | mA |
| RF AGC Delay Voltage Range | - | 1.7 to 2.4 | V |
| AFT (Pulse Width Modulator AFT Input 0.5 V or Open) | | | |
| PWM Frequency | - | 5.0 | MHz |
| vco | | | |
| Tuning Range Gain Select Pulled–Up with 2.0 $k\Omega R$ Gain Select Open | | 1.0 100 | MHz kHz |
| Tuning Voltage | - | 0.5 to 4.5 | V |
| Phase Noise 10 kHz 1.0 kHz | - | -95 -80 | dB |

MC44306

Figure 1. MC44306 Test Circuit



PIN CONNECTIONS

(Top View)

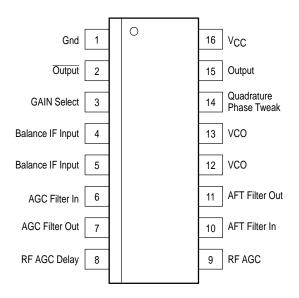
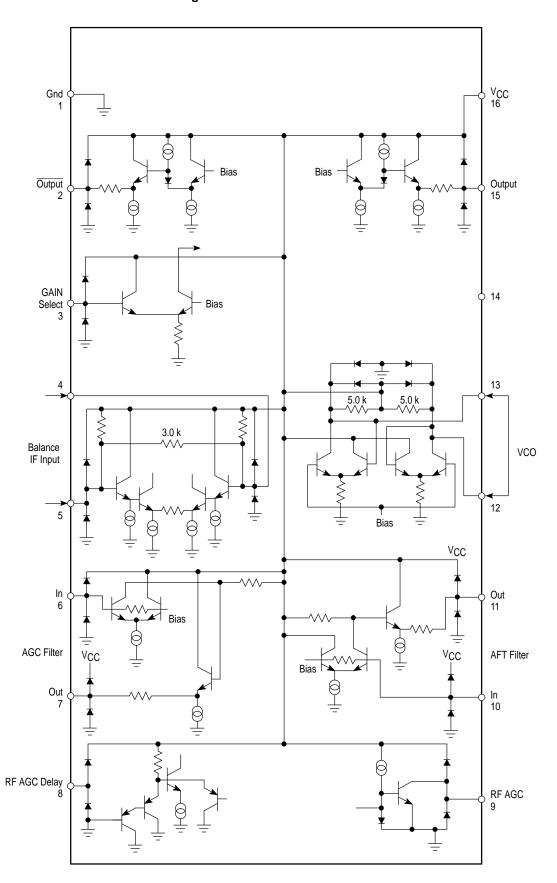
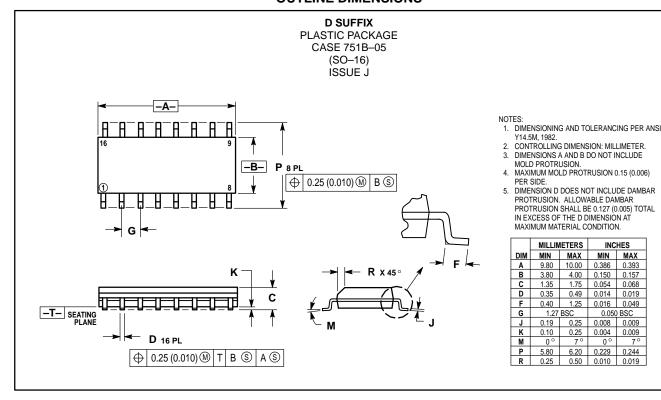


Figure 2. MC44306 Pin Schematic



MC44306

OUTLINE DIMENSIONS



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